

S/N 09/964586

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kristopher Frutschy et al.	Examiner:	Nitin Parekh
Serial No.:	09/964586	Group Art Unit:	2811
Filed:	September 28, 2001	Docket:	884.A93US1
Title:	ARRANGEMENTS TO SUPPLY POWER TO SEMICONDUCTOR PACKAGE		
Assignee:	Intel Corporation	Customer No.:	21186

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

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Serial No :09/964586

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Assignee: Intel Corporation

Page 2

Dkt: 884.A93US1 (INTEL)

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KRISTOPHER FRUTSCHY ET AL.

By their Representatives,

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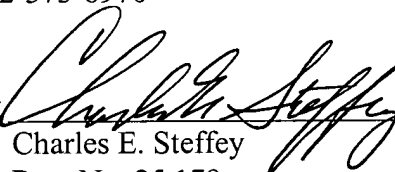
Minneapolis, Minnesota 55402

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Date

December 18, 2003

By



Charles E. Steffey

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 18 day of December, 2003.

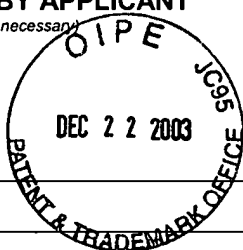
Name

KACIA LEE

Signature

Kacia Lee

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	09/964586
	Filing Date	September 28, 2001
	First Named Inventor	Frutschy, Kristopher
	Group Art Unit	2811
	Examiner Name	Parekh, Nitin
Sheet 1 of 1	Attorney Docket No: 884.A93US1	



US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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	DE-4332115	03/23/1995	Karch, Martin, et al.	H05K	7/20	
	EP-0865082	09/16/1998	Shibamoto, Masanori, et al.	H01L	23/40	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Method of Heat Sinking an Array of Discrete Electronic Packages", Kenneth Mason Publications, Hampshire, GB, No. 342, Research disclosure, XP 000326724, (October 1, 1992), p. 803	

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